

100

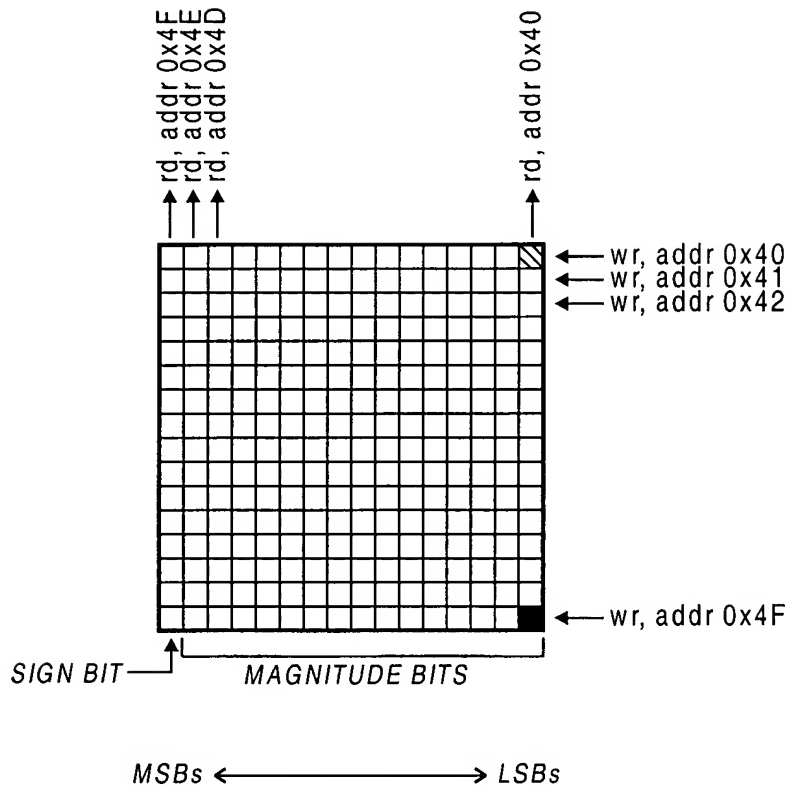
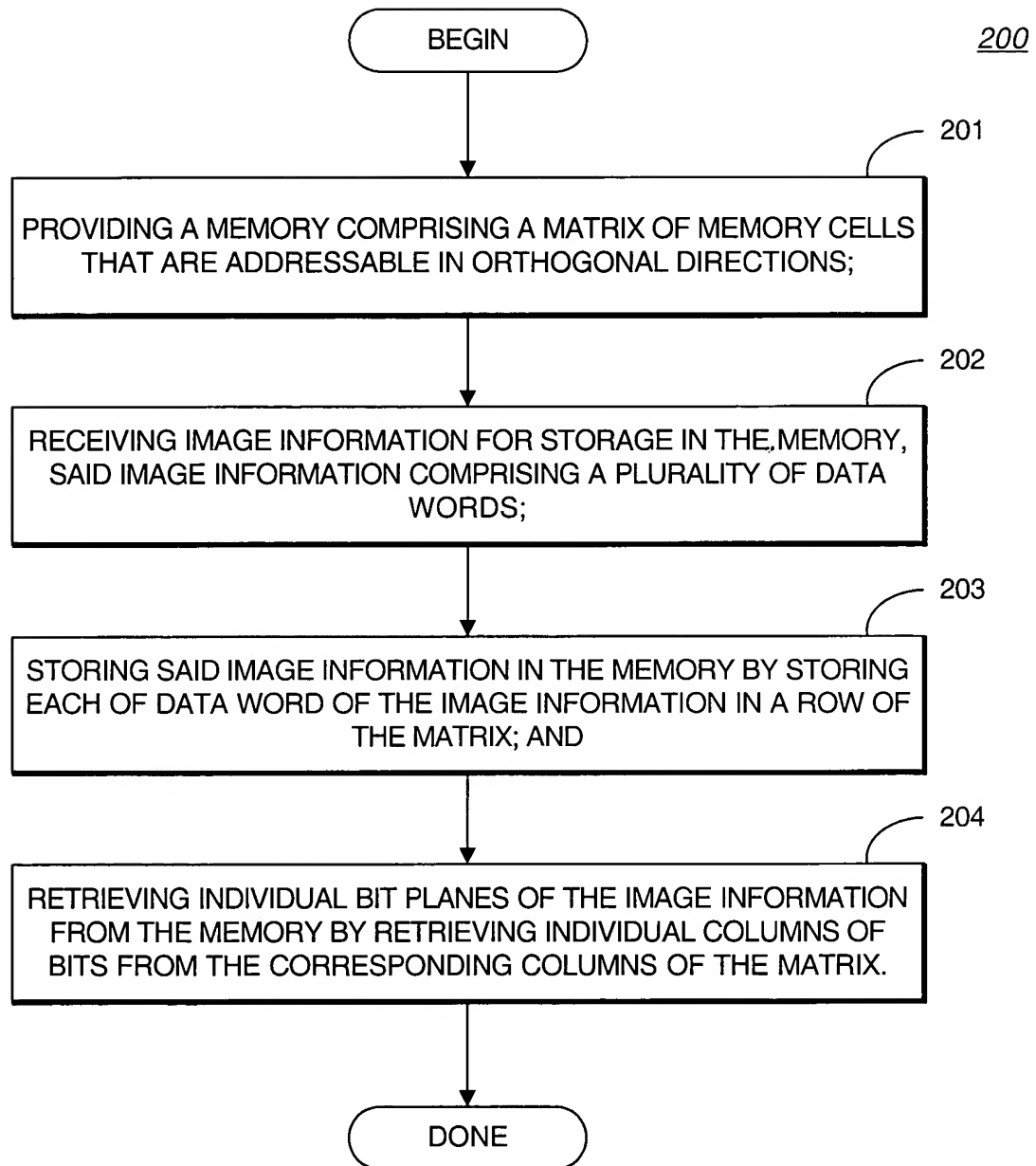
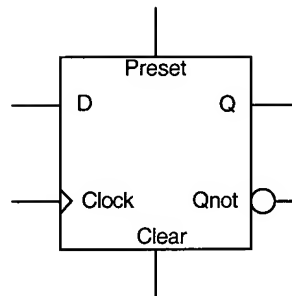


FIG. 1

*FIG. 2*

300

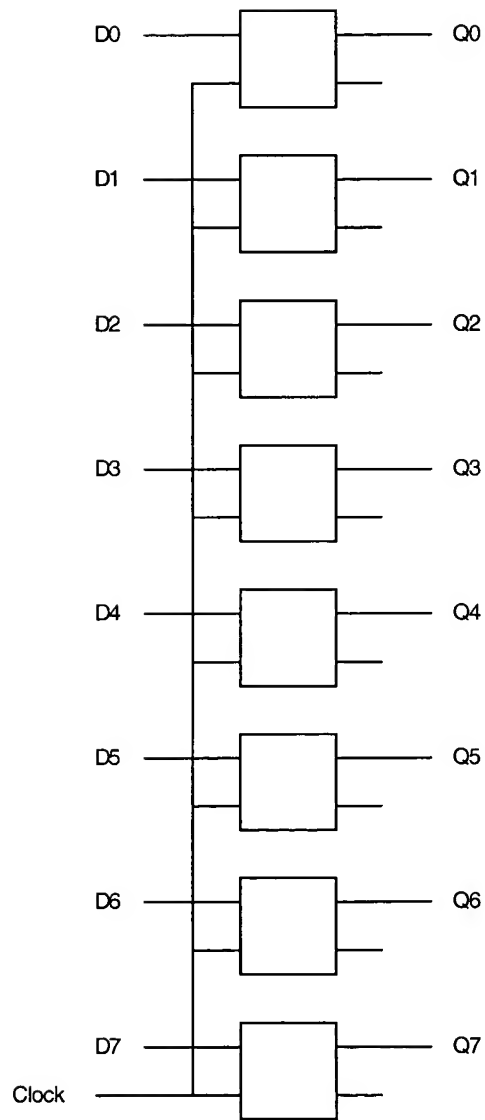
D-type flip-flop

*FIG. 3A*

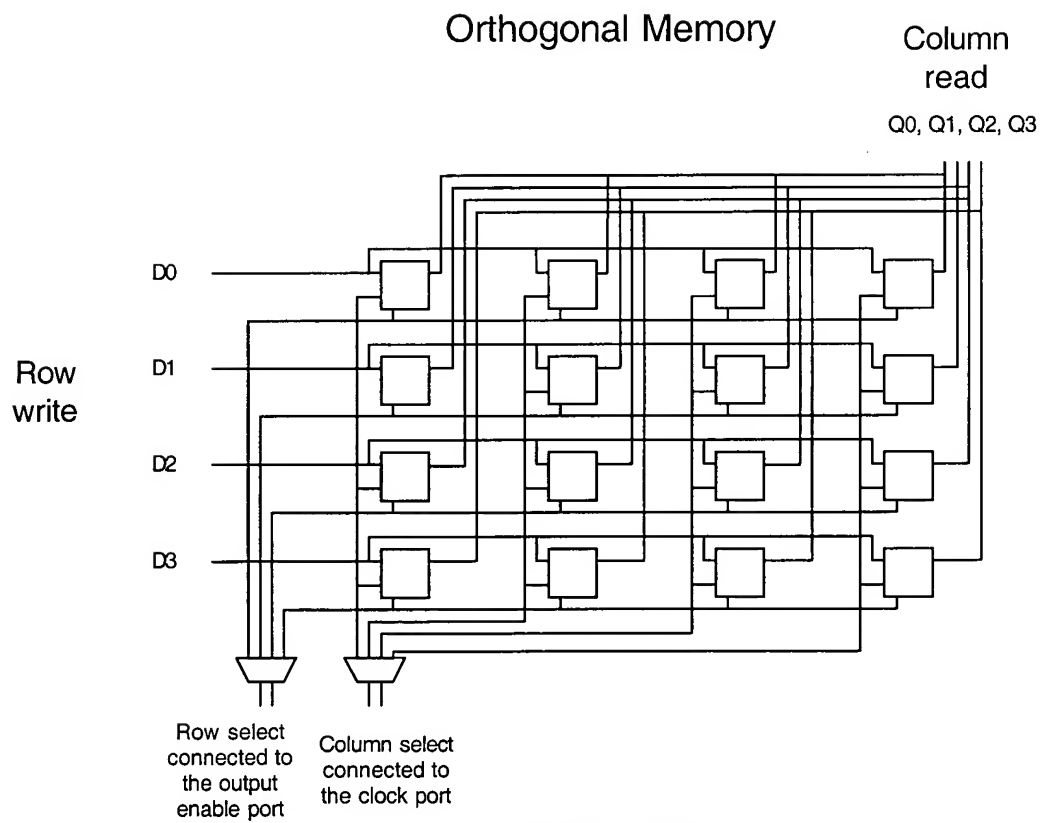
2024710" 00/2500F

310

8 bit register

*FIG. 3B*

2025-07-01 00:25:00

320*FIG. 3C*